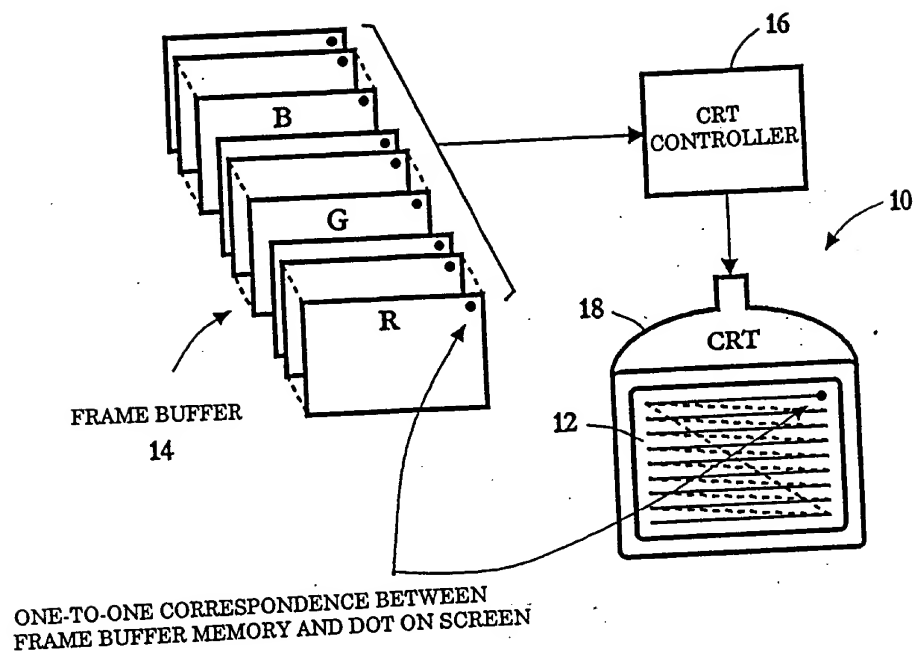


FIG. 1



STRUCTURE OF RASTER-MONITOR CRT

FIG.2

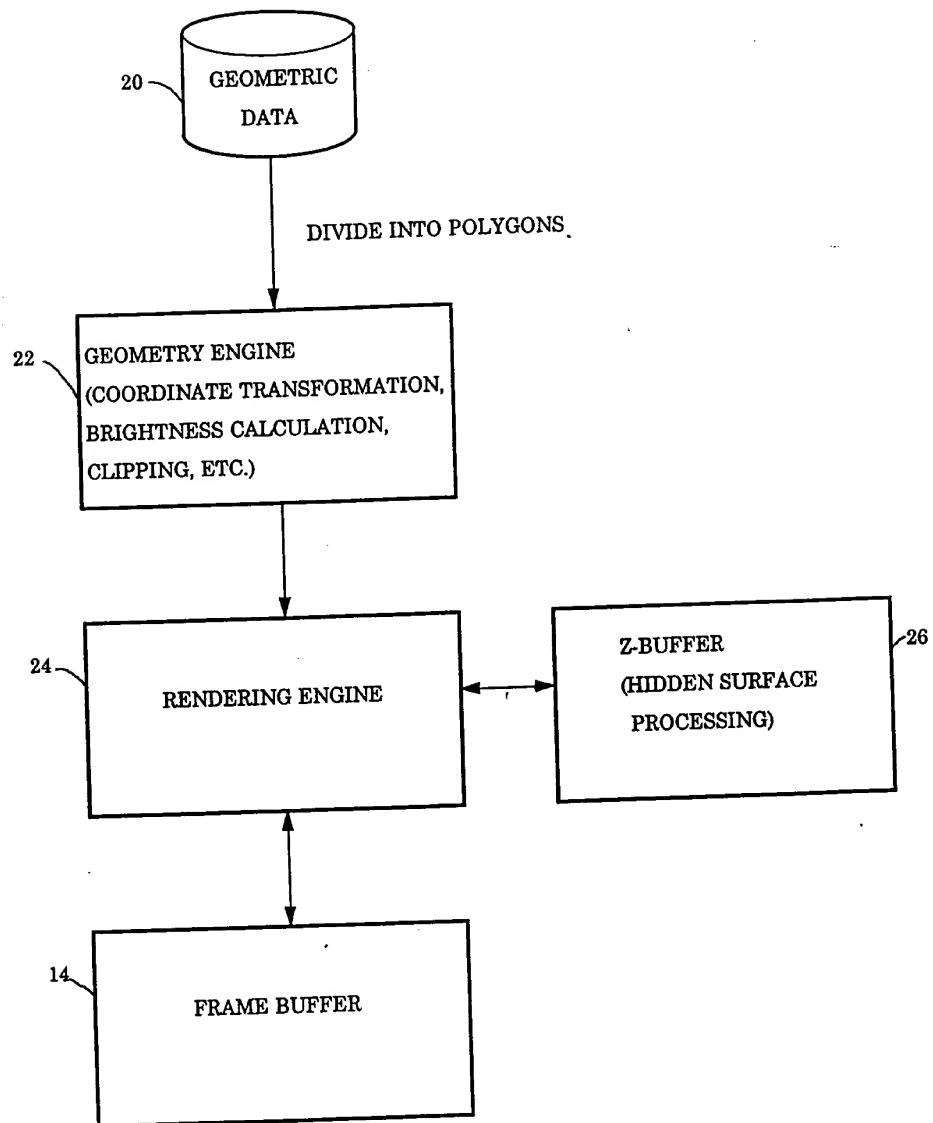
3D GRAPHICS PROCESSING

FIG.3

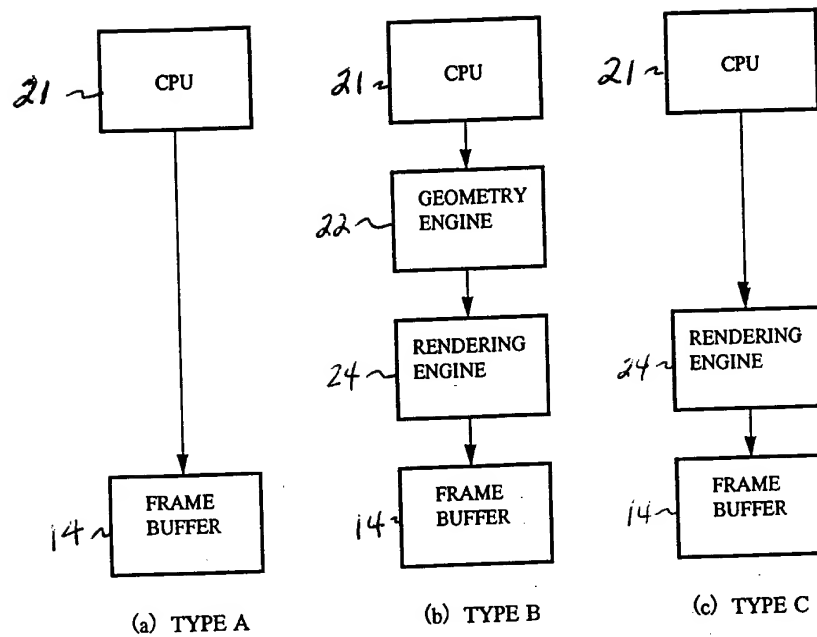
GRAPHICS ARCHITECTURE

FIG.4

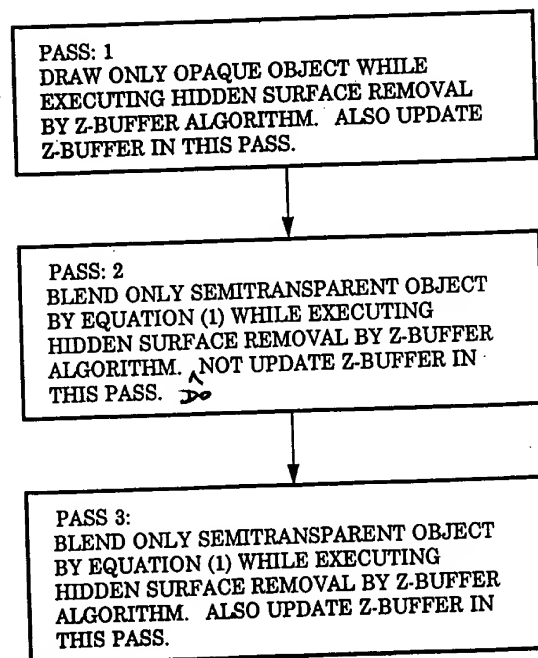


FIG. 5

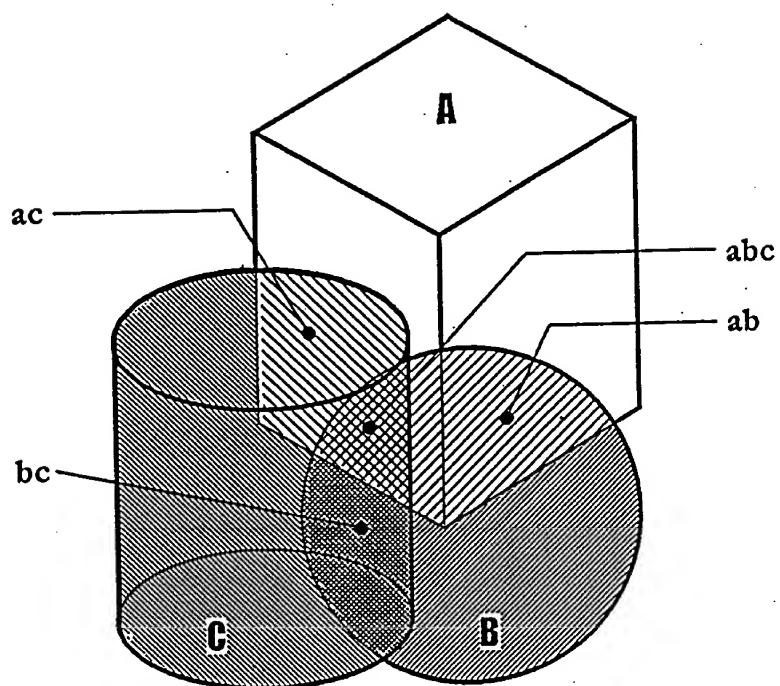
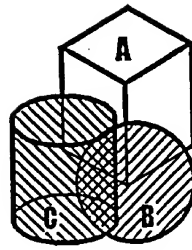
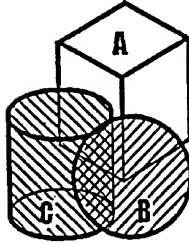


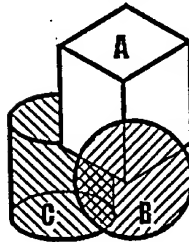
FIG. 6



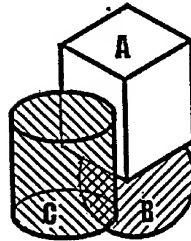
$$C < B < A$$



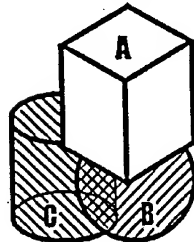
$$B < C < A$$



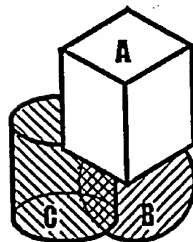
$$B < A < C$$



$$C < A < B$$



$$A < B < C$$



$$A < C < B$$

FIG. 7

